

## AMRITA VISHWA VIDYAPEETHAM

Amritanagar P.O., Ettimadai, Coimbatore - 641 112.

Academic Block-2 SF VLSI Lab-I (Total PCs: 48 Nos.)								D302	
Even Semster Lab Time Table 2021-2022									
Timings	8.40AM TO 9.30AM	9.35AM TO 10.25AM	10.30AM TO 11.20AM	11.25AM TO 12.15PM	12.20PM TO 1.10PM	1.10PM TO 2.10PM	2.10PM TO 3PM	3.05PM TO 3.55PM	4PM TO 4.50PM
Day/Period	1	2	3	4	5	6	7	8	9
<b>Monday</b>	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,	15ECE499 Project Phase II, 8th Semester B. Tech. ECE,	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,				21VL683 ASIC Design & FPGA Lab 2nd Semester M. Tech. VLSI Design Mr. N. Mohankumar		
<b>Tuesday</b>		21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,					15ECE499 Project Phase II, 8th Semester B. Tech. ECE,	15ECE499 Project Phase II, 8th Semester B. Tech. ECE,	
<b>Wednesday</b>	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,		21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,	15ECE499 Project Phase II, 8th Semester B. Tech. ECE,	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design, Dr.Ramesh S R, Dr.D S Harish Ram, Dr.J P Anita, Dr.M.Nirmala Devi			
<b>Thursday</b>	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,		21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,		21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,		21VL684,2nd Semester M. Tech. VLSI Design, Functional Verification Lab Dr. B. Karthi		
<b>Friday</b>	15ECE499 Project Phase II, 8th Semester B. Tech. ECE,	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,	15ECE499 Project Phase II, 8th Semester B. Tech. ECE,		15ECE499 Project Phase II, 8th Semester B. Tech. ECE,	21VL799- Project Phase II . 4th Semester M. Tech. VLSI Design,		