

**AMRITA VISHWA VIDYAPEETHAM**  
**AMRITA SCHOOL OF ENGINEERING**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**VLSI LABORATORY – AB II, D 201**  
**2026 – 27 Odd Semester**

**Faculty Incharge: Mr. Mohan Kumar**

**Technical Staff: Ms. A Arul vadivel**

TIME/DAY	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII
	8.00-8.50	8.50-9.40	9.40-10.30	10.45-11.35	11.35-12.25	12.25 -1.15	1.15 - 2.05	2.05 - 2.55	2.55 - 3.45	3.45 - 4.35	4.35- 5.25	5.25- 6.15
MONDAY				25VL681 1 <sup>st</sup> VLSI				23ECE383-ECE-A				
TUESDAY				23ECE383-ECE-B				23CCE281-CCE-B				
WEDNESDAY		23ECE498/23CCE498		1st sem M.Tech VLSI				25VL682 1 <sup>st</sup> VLSI				
THURSDAY		23ECE498/23CCE498		1st sem M.Tech VLSI				23ECE383-ECE-C				
FRIDAY		23ECE498/23CCE498		23ECE102-ECE B				23CCE281-CCE-A				