

Amrita Vishwa Vidyapeetham, Coimbatore
Academic Block-2 First Floor VLSI DESIGN LABORATORY (D-201)
[Desktop PC- 65 Nos.]

ODD Semester 2026-2027 - Computer Lab Timetable

Day/ Timings	8.50 am - 9.40 am	9.40 am - 10.30 am	10.45 am - 11.35 am	11.35 am - 12.25 pm	12.25 pm - 01.15 pm	01.15 pm - 02.05 pm	02.05 pm - 2.55 pm	2.55 pm - 3.45 pm	3.45 pm - 4.35 pm
Monday			25VL681 I M.Tech VLSI				23ECE383 VLSI Design Laboratory V CSE A Dr. S. R. Ramesh Mr. N. Mohankumar, Dr. Navya Mohan		
Tuesday			23ECE383 VLSI Design Laboratory V CSE B Dr. Abhishek Raj Dr. S. R. Ramesh, Dr. E. Prabhu				23CCE281 Microcontrollers and Interfacing Lab III CCE B Dr. S. Kamatchi Dr. J. Rolant Gini, Dr. Aditya S		
Wednesday	23ECE498/23CCE498		I M.Tech VLSI				25VL682 I M.Tech VLSI		
Thursday	23ECE498/23CCE498		I M.Tech VLSI				23ECE383 VLSI Design Laboratory V CSE C Dr. E. Prabhu Dr. Aditya S, Dr. B. Yamuna		
Friday	23ECE498/23CCE498		23ECE102 I ECE B				23CCE281 Microcontrollers and Interfacing Lab III CCE A Dr. Harikumar M. E Mr. C. B. Rajesh, Dr. S. Kamatchi		