

**Amrita Vishwa Vidyapeetham, Coimbatore**  
**Academic Block-2 First Floor Digital Signal Processing (DSP) Lab (D-202)**  
**[Desktop PC- 68 Nos.]**

**ODD Semester 2026-2027 - Computer Lab Timetable**

Day/ Timings	8.50 am - 9.40 am	9.40 am - 10.30 am	10.45 am - 11.35 am	11.35 am - 12.25 pm	12.25 pm - 01.15 pm	01.15 pm - 02.05 pm	02.05 pm - 2.55 pm	2.55 pm - 3.45 pm	3.45 pm - 4.35 pm
Monday	23ECE102- ECE A		24CYS302 Secure Coding Sem V B.Tech CYS A Mr. Sai Vignesh				23ECE283 ECE B		
Tuesday	23CCE101- CCE A			23ECE283 ECE C			24CYS202 User Interface Design Sem III B.Tech CYS B Mr. Sibi S		
Wednesday	23ECE283 ECE A			23ECE498/23CCE498			24CYS281 Computer Networks Lab Sem V B.Tech CYS B Dr. Sindhu V		
Thursday			25BM606-Lab				23ECE102- ECE C		
Friday	23ECE498/23CCE498						23CCE101- CCE B		